

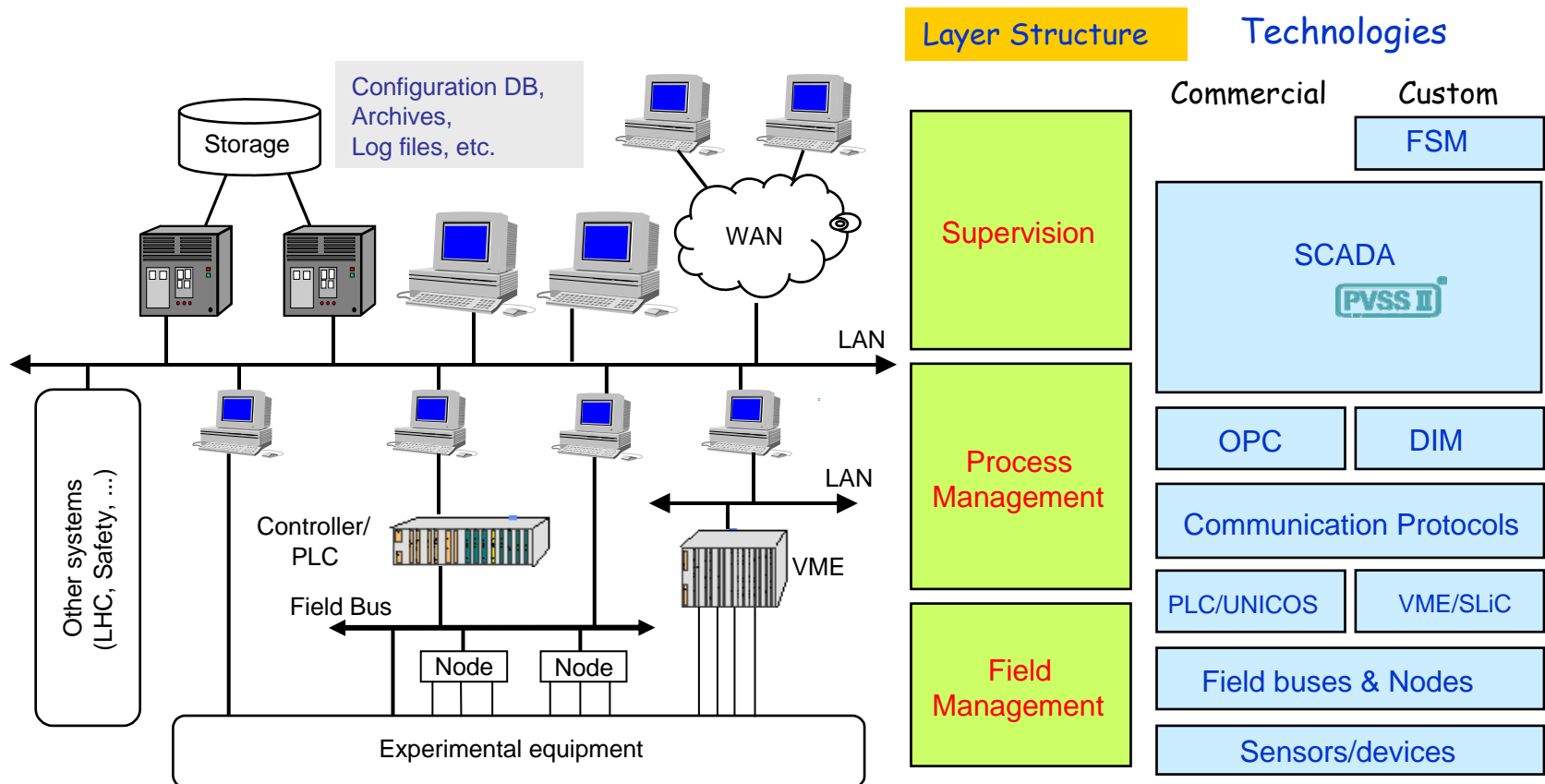
Detector Control Systems

A software implementation: Cern Framework + PVSS

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Hardware and Software

Logically any DCS has a 3 layers hardware structure:
supervision -> control -> field



Requirements: architecture

- Client/Server architecture with hardware abstraction layers
 - Servers execute tasks and provide data independently of the clients
- Hierarchical mechanism (tree structure)
 - FSM (finite state machines): “nodes” with 1 parent and many children
 - easy partitioning
 - distributable system, possible decentralized decision making and error recovery

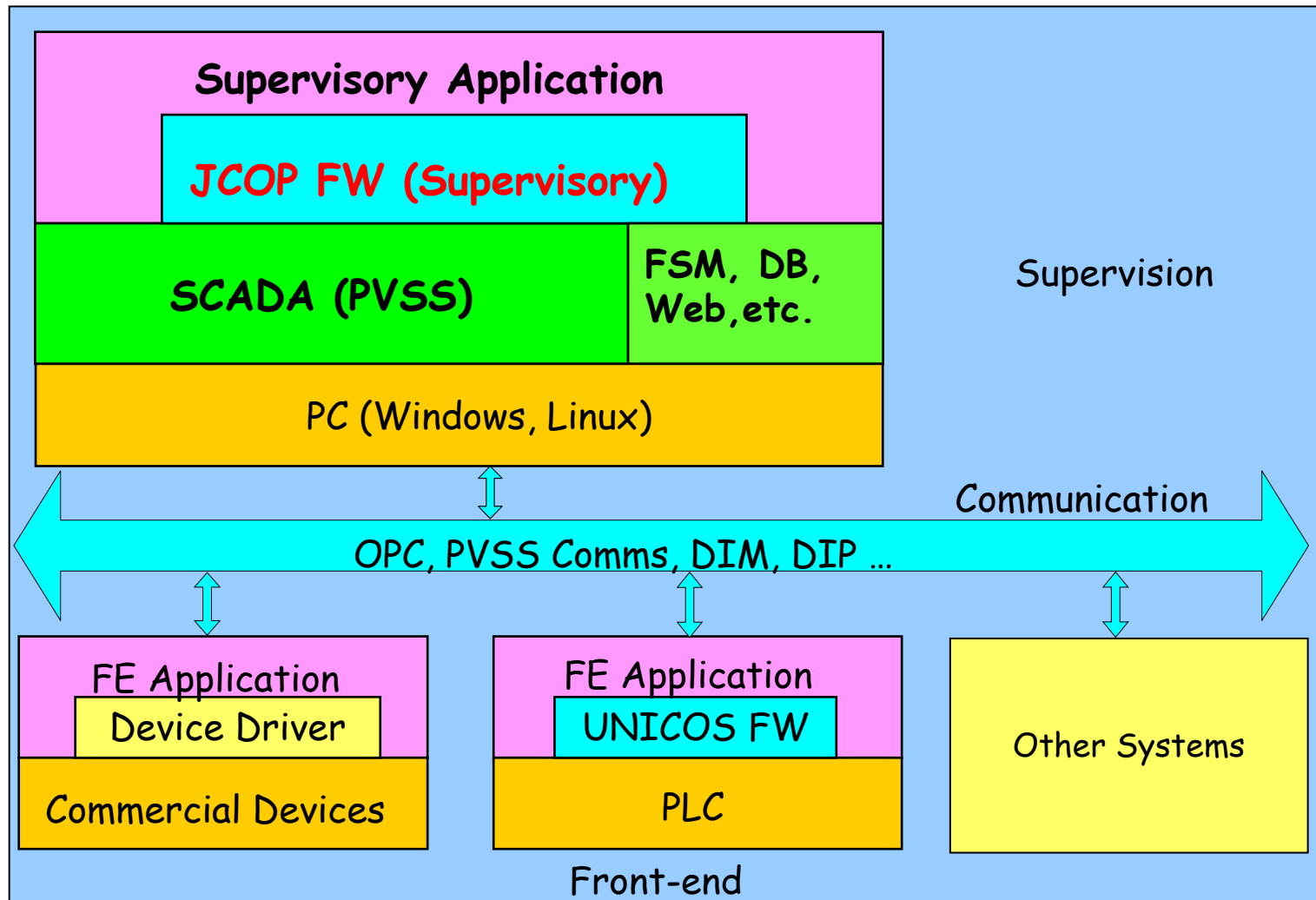
Requirements: implementation

- reliability
- flexibility, expandability
- low cost
- short development time
- ease of use (developers and users)
- documentation/support

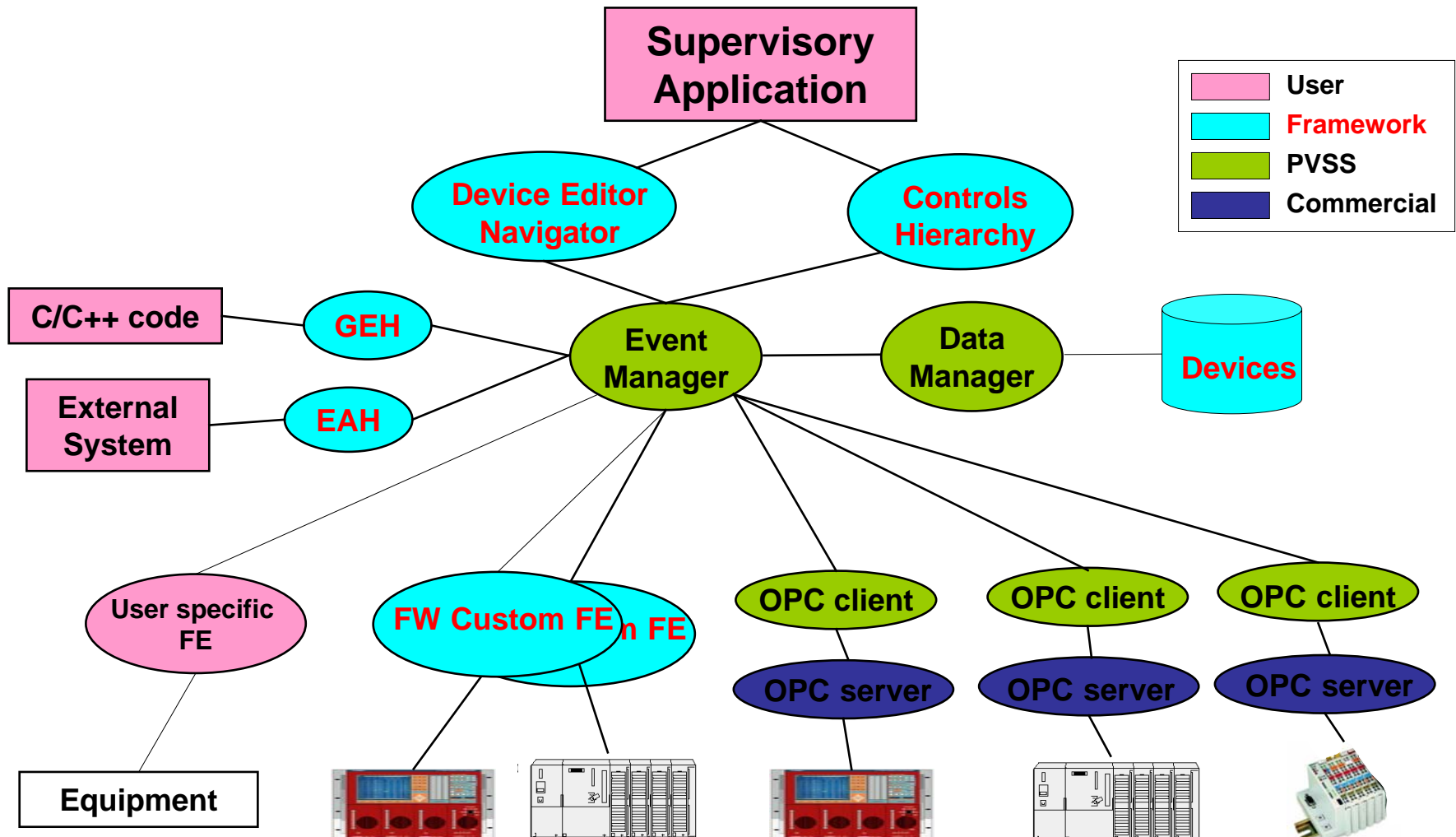
CERN choice: JCOP + PVSS

- What it is PVSS:
 - Commercial software by ETM (Austrian company)
 - SCADA (supervisory control and data acquisition)
 - Run-time DB, archiving, logging, trending
 - Alarm generation and handling
 - Device access (OPC/DIM, drivers) and control
 - User data processing (C-style scripting language)
 - Graphical editor for user interface
- What is JCOP (Joint COntrol Project)
 - CERN developed a framework for PVSS
 - Simple interface to PVSS
 - Implements hierarchy (FSM)
 - Provides drivers for most common HEP devices
 - Many utilities (eg: graphics)

JCOP + PVSS



JCOP + PVSS



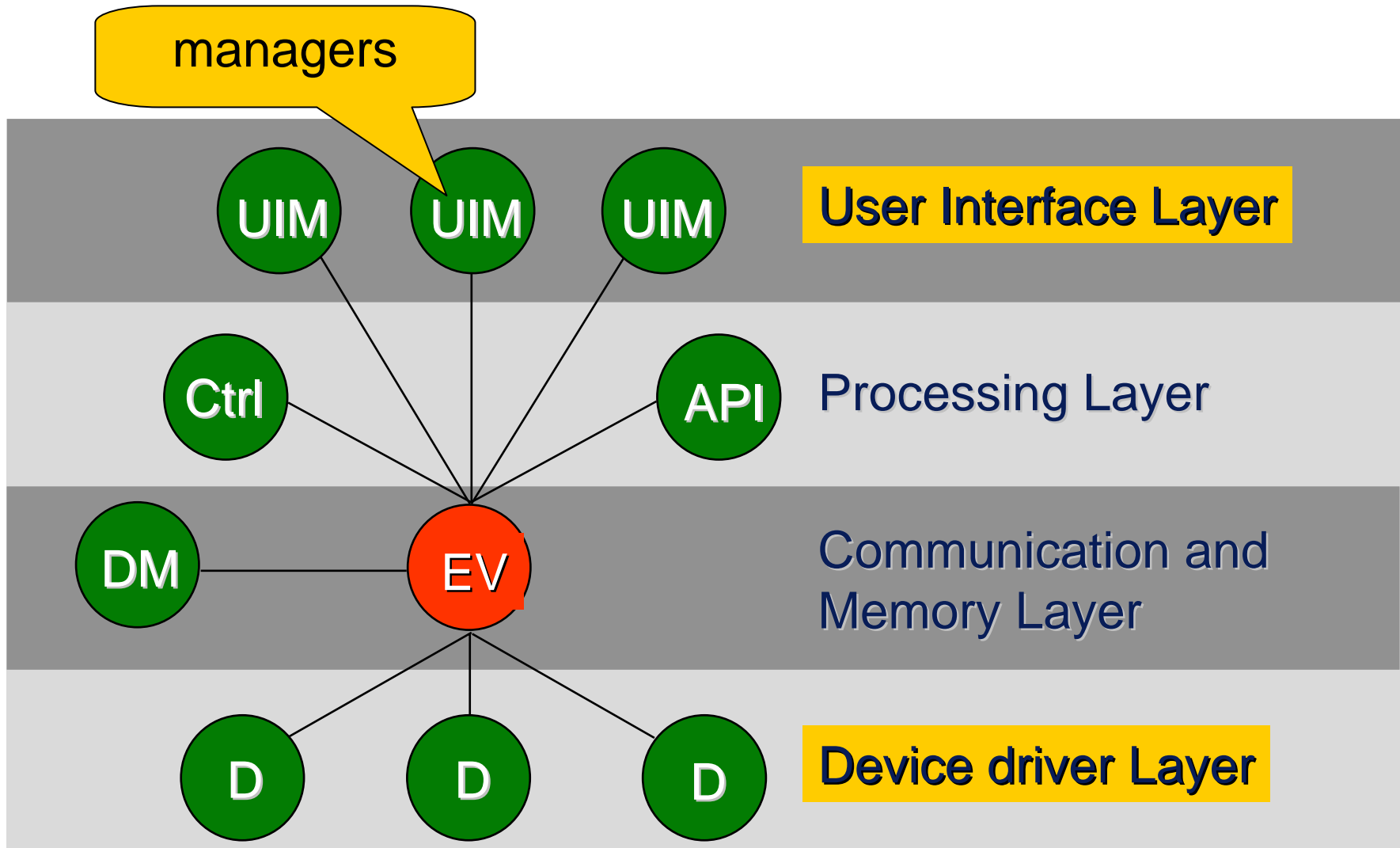
JCOP features

- Provides a **complete** component set for:
 - CAEN HV power supplies
 - Wiener crates and LV power supplies
 - Other power supplies (ISEG, ELMB)
 - Generic devices to connect analog or digital I/Os
- “complete” means:
 - any necessary OPC/DIM servers;
 - Device modeling (mapping of PVSS data-points to device values)
 - scripts, libraries and panels to configure and operate the device.
- Other tools to integrate user's devices

PVSS II features

- Distributed architecture: several processes for different tasks (“managers”) run separately and communicate internally via TCP/IP
- Managers subscribe to data (“subscribe on change” mode)
- Event manager is the heart of the system
- Device oriented (abstraction of complex devices)
- Devices are modeled using “Data-Points”:
 - All data relative to a device is grouped together and may be reached hierarchically in C++ style (eg: `crate.board.channel.vMon`)

PVSS II system architecture



PVSS: how it was chosen

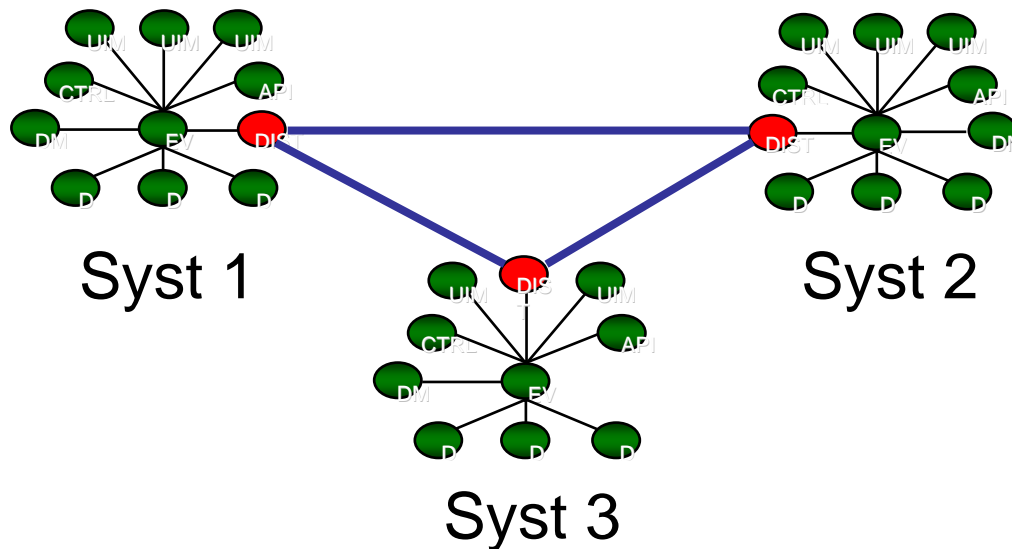
- CERN chose PVSS in 1999 among >40 products based on a set of “objective” criteria:
 - Scalability
 - Cross-platform
 - Archiving and trending ability
 - Remote access
 - Security
 - Alarm handling
 - Extensibility and ease of use
- It's been a long evaluation process (lots of tests)
- Much development has been done since then

JCOP+PVSS advantages

- Scalability: practically no limits (see next page)
- Stability of kernel
- Flexibility (customization, easy integration of user functions)
- Win AND Linux (not OR)
 - managers of the same system may run on different platforms
 - may develop on one platform and run on the other
 - only limitation: OPC client/server must run on Win
- By now is tried and tested (Compass, Harp, NA60, all 4 LHC)
- HV and LV are “Plug and Play” (drivers and modeling)
- Easy partitioning (commissioning and calibrations)
- Documentation (cern site, not ETM)
- Easy remote access on the Web through a web server
 - user’s GUI get downloaded by the remote browser
 - claim to support any security option (well...)
- Very flexible alarm handling scheme
- DB: proprietary or Oracle
- Redundancy (double system with automatic switchover)
- Some nice safety features if system is overloaded

(follows) Scalability

- (Cern is) not aware of any built-in limit
 - As many managers as needed (all communications handled automatically)
 - **Scattered system** (one system running on many computers)
 - **Distributed systems**: multiple systems connected together and exchanging data



Dis-advantages

- **COST !!!**
 - have no idea: should investigate with ETM (koller@etm.at)
 - complex licensing model: usually possible to negotiate special deals
- Maybe even too “big” ?
- Still need to develop part of the device drivers (but this is unavoidable)
- Support by ETM in the US ?
- Cern will not commit to any *formal* support but “this does not mean we will not help you if we could” (Wayne.Salter@cern.ch)

Example: CAEN HV

See Novahv Board definition in terminal data point editor
This is the kind of menu you can see

your data-points with

You only need to configure your device settings, alarms, plots, etc

The screenshot displays three overlapping windows from the CAEN HV software interface.

Device Editor & Navigator: This window shows a tree view of available devices. The 'CAEN' folder is expanded, and a context menu is visible with options like 'Add...', 'Clone...', 'Remove...', 'Settings...', 'Properties...', 'Expand...', and 'Configure...'. The 'Available' section lists various device types such as 'fwCaen', 'fwConfig', 'fwDIM', 'fwDIP', 'fwGenericB', 'fwTrending', and 'fwWiener'.

Edit Device/Model Definition: This window is used for configuring a specific device. It shows the 'Edit device definition' tab with a 'Type' of 'CAEN Channel' and a 'DP Type' of 'FwCaenChannel'. The 'Model' is set to 'Choose a device model...'. Below this, the 'Editing: FwCaenChannelInfo' section is visible, with tabs for 'General', 'Panels', 'Properties', 'Address', 'OP funct', 'Archive', 'Alarm', 'Smooth', 'Convers', and 'P'. The 'OPC items' tab is selected, showing a table of OPC items.

num	names	dps	items
16	i1 set point readback	.readBackSettings.i1	%name3%.board%pos2%.Ch
17	Ramp down speed readback	.readBackSettings.rDown	%name3%.board%pos2%.Ch
18	Ramp up speed readback	.readBackSettings.rUp	%name3%.board%pos2%.Ch
19	Trip time readback	.readBackSettings.tripTime	%name3%.board%pos2%.Ch
20	v0 set point readback	.readBackSettings.v0	%name3%.board%pos2%.Ch
21	v1 set point readback	.readBackSettings.v1	%name3%.board%pos2%.Ch
22	Voltage software limit readback	.readBackSettings.vMaxSoftValue	%name3%.board%pos2%.Ch
23	Set survey object	.setSurveyObject	EMPTY
24	i0 set point	.settings.i0	%name3%.board%pos2%.Ch
25	i1 set point	.settings.i1	%name3%.board%pos2%.Ch
26	Switch on/off	.settings.onOff	%name3%.board%pos2%.Ch
27	Ramp down speed	.settings.rDown	%name3%.board%pos2%.Ch
28	Ramp up speed	.settings.rUp	%name3%.board%pos2%.Ch
29	Trip time	.settings.tripTime	%name3%.board%pos2%.Ch
30	v0 set point	.settings.v0	%name3%.board%pos2%.Ch
31	v1 set point	.settings.v1	%name3%.board%pos2%.Ch
32	Voltage max soft value	.settings.vMaxSoftValue	%name3%.board%pos2%.Ch
33	Software enabled	.softwareEnabled	EMPTY
34	Survey on/off	.surveyOffOn	EMPTY

Vision_1: Datapoint parameterization: This window shows a tree view of datapoints. The 'CAEN/novahv/board00' folder is expanded, showing sub-folders like 'alert_hdl', 'common', 'lock', 'BdStatus', 'Information', 'HVMMax', 'Temp', 'model', and 'Status'. The 'Temp' folder is expanded, showing 'common', 'lock', 'Temp#EU', 'Temp#HighEU', and 'Temp#LowEU'.

Performance

- Report tests made at Cern in 2003/6
 - Distributed 16-system in 3-level tree on 16 PC of various type, each system with ~40K DPE
 - Tested up to 260K DPE on one PC (non-realistic test 5M DPE on top PC of a 130-system in 5-level tree)
 - Total # of DPE not significant in subscribe-on-change mode. What overload a system is the # of changes/s
 - P4 CPU:2.4GHz RAM:2Gb running EM + DM + 5 managers
 - saturate CPU at 1600 changes/s (500 ch/s = 35%CPU)
 - when moving EM outside saturates CPU at 2800 changes/s
 - alarm handling and archiving: saturate at ~700 alarms/s
 - Dual-CPU and large RAM are well exploited
 - With 400K DPE (includes FEB)
 - 1% changes/s : 1+2 CPU tree
 - 5% changes/s : 1+10 CPU tree

Other possibilities

- EPICS: see Andrew Norman presentation
- iFix (by Intellution):
 - Commercial
 - Slow (see CDF experience)
 - Fragile connections between nodes
 - No drivers included
 - Windows only
 - Limit 100,000 channels ?
- LabView:
 - No large systems
- A lot of other commercial software out there, but should be tested...